


FACULTEIT INGENIEURSWETENSCHAPPEN  
EN ARCHITECTUUR

DEPARTMENT OF INFORMATION TECHNOLOGY  
PHOTONICS RESEARCH GROUP

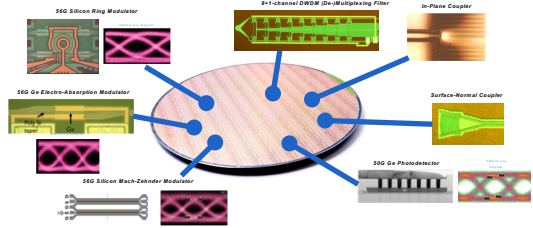
# GAAS NANO-RIDGE LASERS ON SILICON

Y. Shi, M. Baryshnikova, Y. De Koninck, M. Pantouvaki, J. Van Campenhout, B. Kunert, D. Van Thourhout




## SILICON PHOTONICS MISSES A LASER...

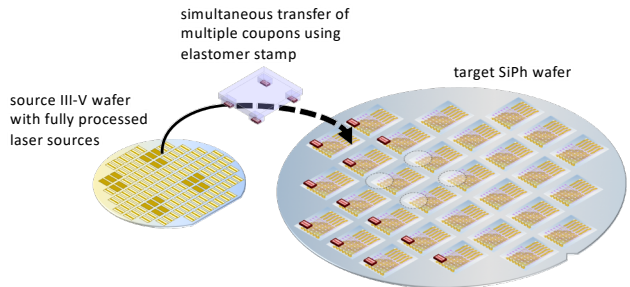
E.g. IMEC iSIPP50G-platform



No laser source → need for a III-V/silicon photonic integration platform



## OUR OPTION #1 : TRANSFER PRINTING




simultaneous transfer of multiple coupons using elastomer stamp

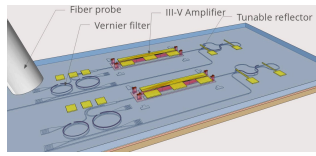
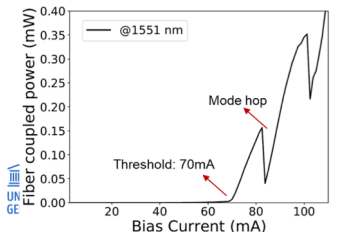
source III-V wafer with fully processed laser sources

target SiPh wafer

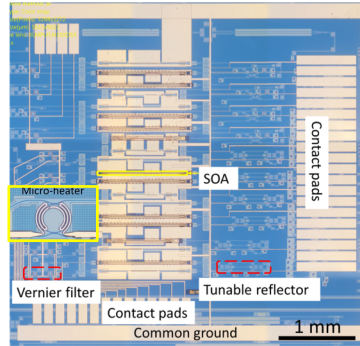
**μTP combines advantages of flip-chip and die-to-wafer bonding**



## III-V-ON-SI WIDELY TUNABLE LASER WITH μTP

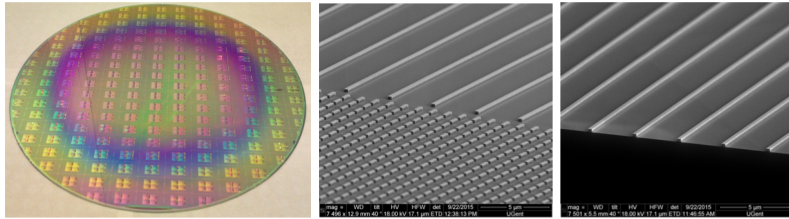
| Bias Current (mA) | Fiber coupled power (mW) |
|-------------------|--------------------------|
| 20                | 0.00                     |
| 40                | 0.00                     |
| 60                | 0.00                     |
| 70                | 0.05                     |
| 80                | 0.15                     |
| 90                | 0.25                     |
| 100               | 0.35                     |



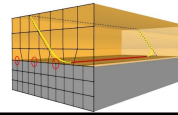
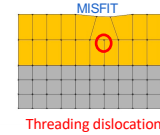
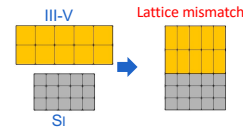
[J. Zhang et al., IEEE ECOC, 2019]

### OUR OPTION #2: DIRECT EPITAXY ON SILICON

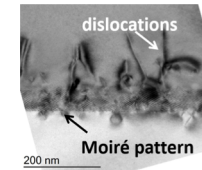
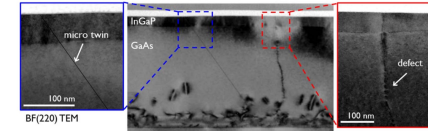
- Why ?
  - Ultimate scalability: selective growth using MOCVD on 300mm wafers
  - It's fun



### HETEROEPITAXY- CHALLENGES



LATTICE MISMATCH RESULTS IN UNWANTED DEFECTS



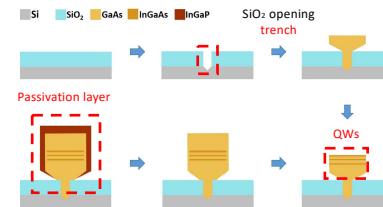
### ASPECT-RATIO-TRAPPING (ART)

■ Si ■ SiO<sub>2</sub> ■ GaAs ■ InGaAs ■ InGaP

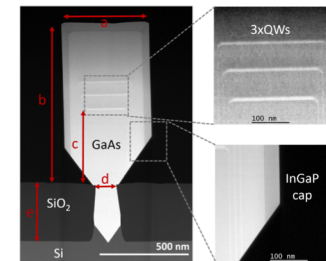


### ASPECT-RATIO-TRAPPING (ART)

Basic processing scheme

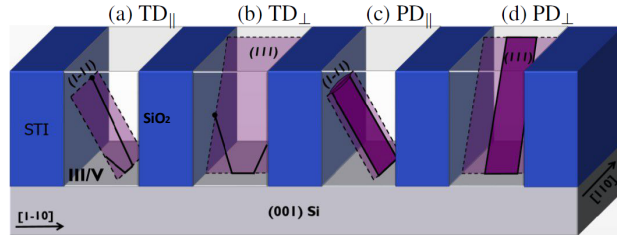


Reference sample



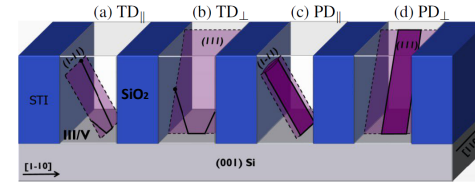
### ASPECT RATIO TRAPPING (ART)

- High-AR SiO<sub>2</sub> trenches enable **trapping of threading dislocations**

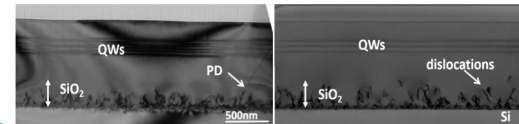


### ASPECT RATIO TRAPPING (ART)

- High-AR SiO<sub>2</sub> trenches enable **trapping of threading dislocations**.



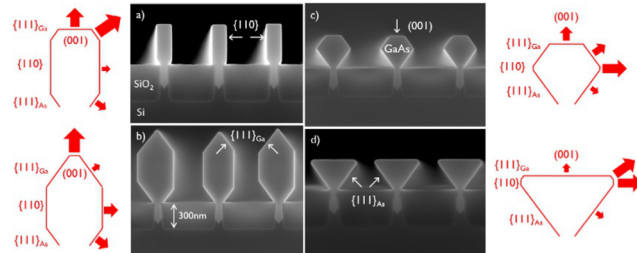
Longitudinal TEM-pictures



TD < 3x10<sup>9</sup>cm<sup>-2</sup>  
(limited by measurement)  
PD < 0.14-0.45μm<sup>-1</sup>

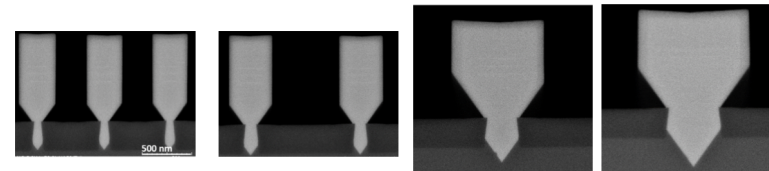
### NANO-RIDGE ENGINEERING (NRE)

Control growth rate on different crystal planes to obtain well-defined nano-ridge profile  
Higher growth rate facets disappear, whereas facets with **lower growth rates** define the nano-ridge profile

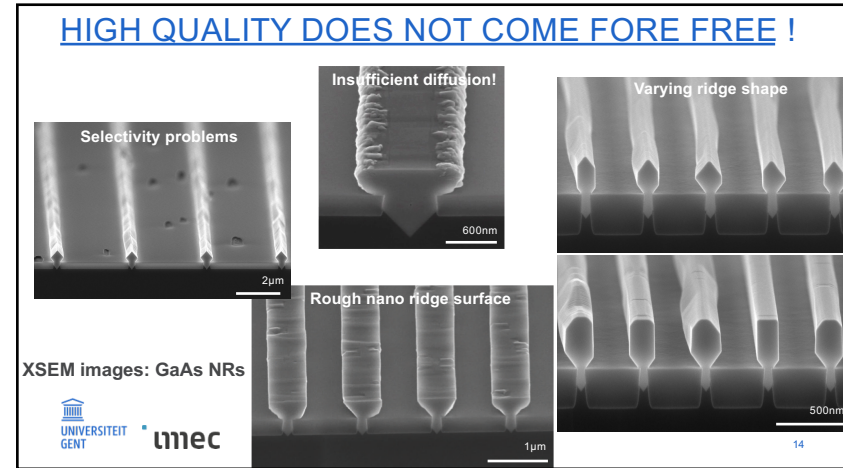
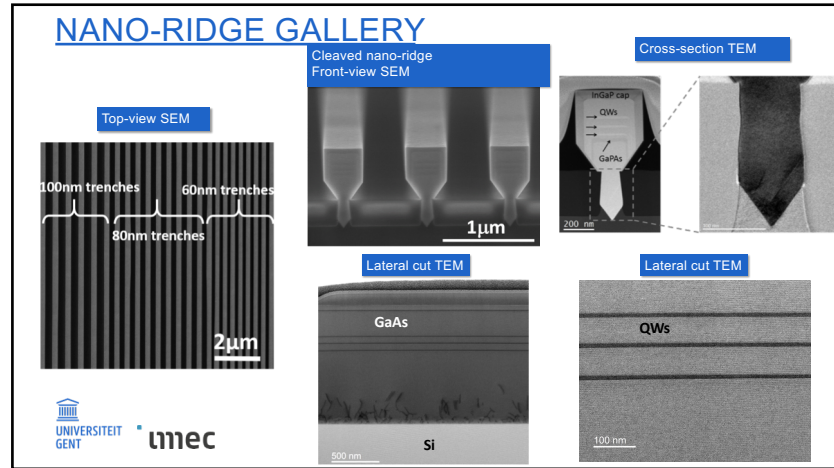


### NANO-RIDGE ENGINEERING (NRE)

60nm trench      100nm trench      300nm trench      500nm trench

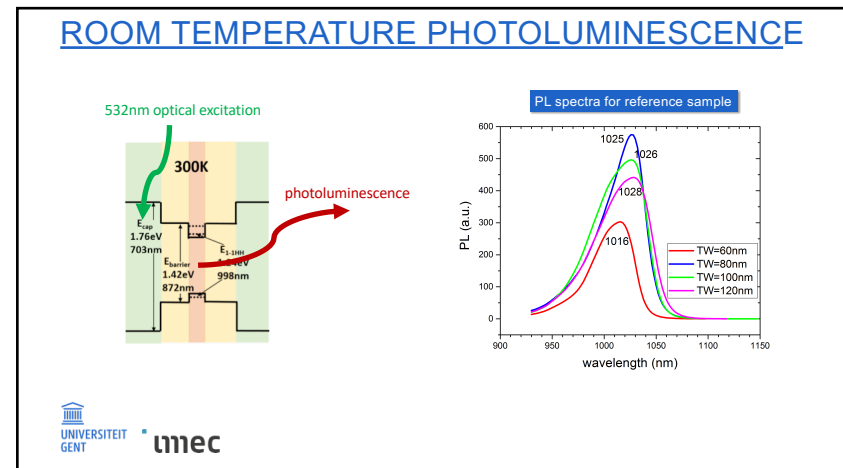


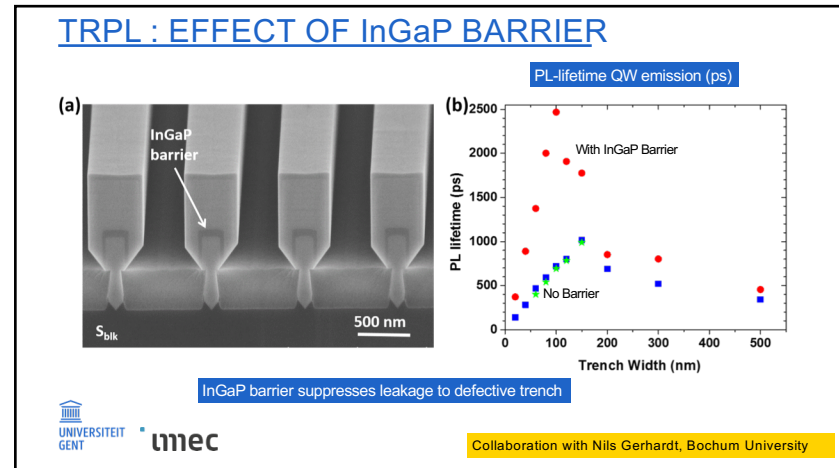
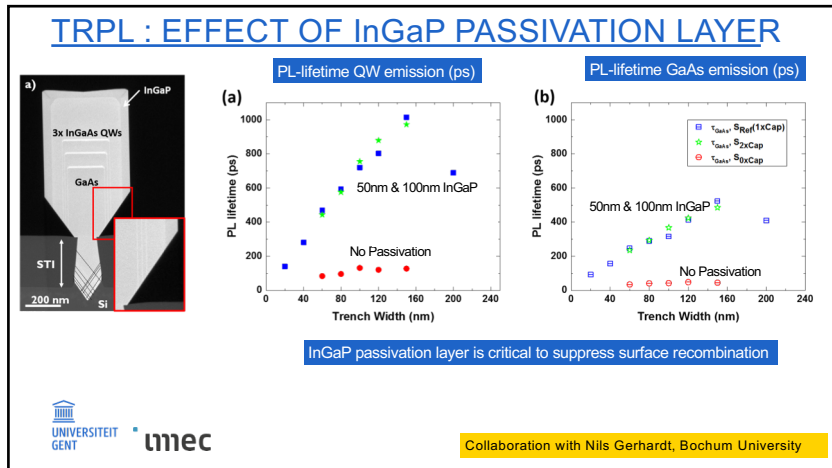
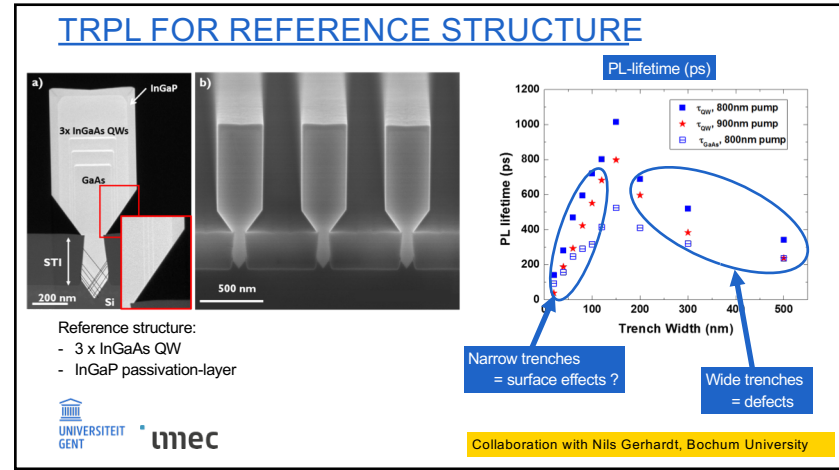
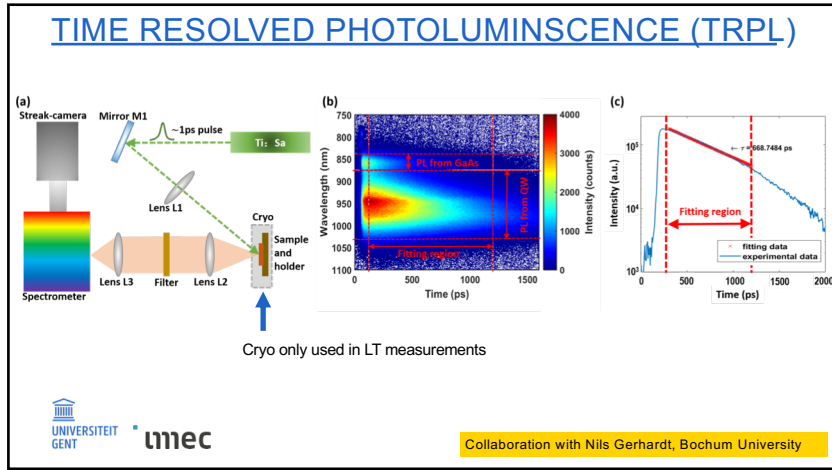
Increasing trench width: shape and volume of nano-ridge changes

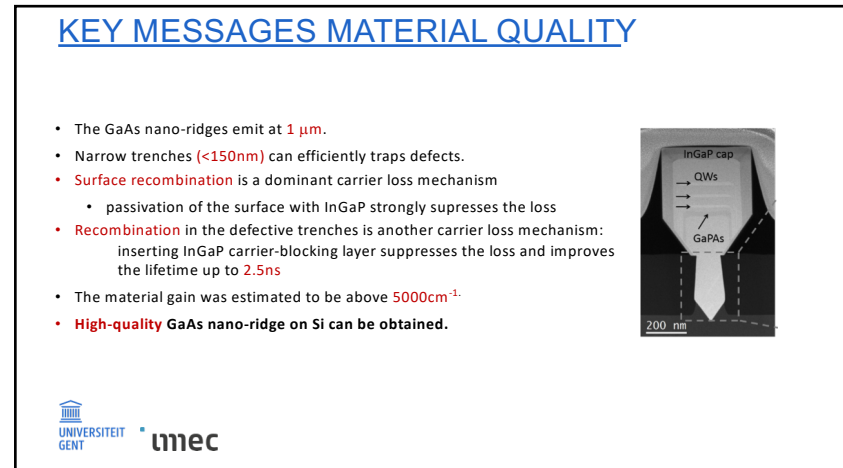
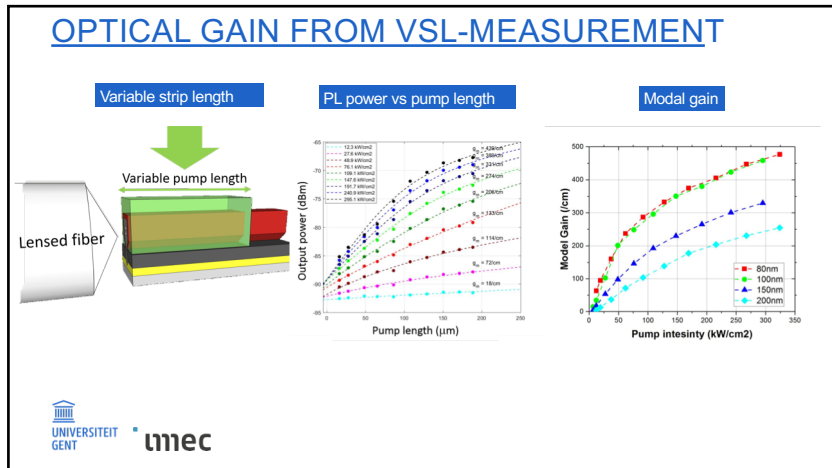
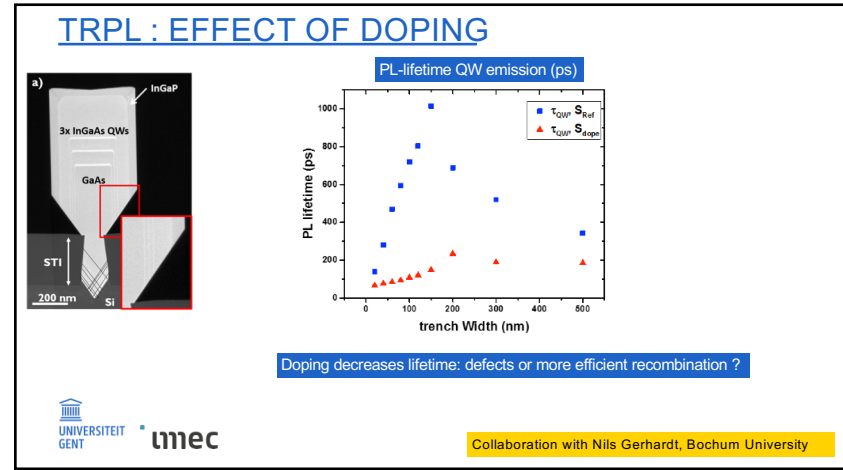
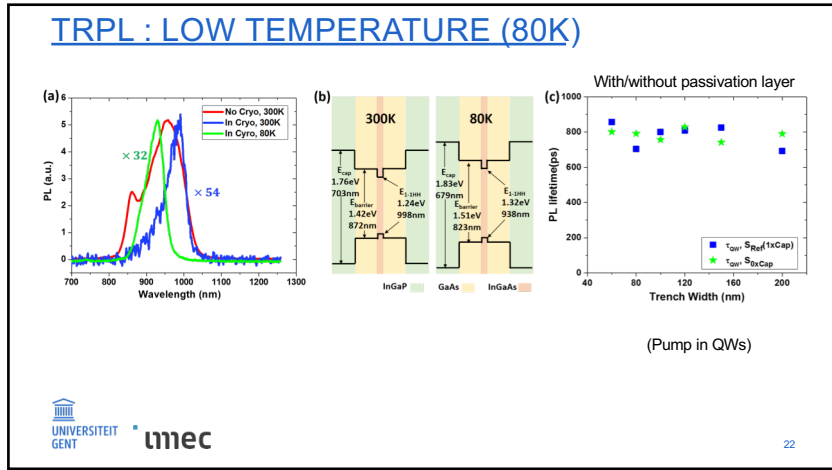


# BASIC CHARACTERIZATION OPTICAL PROPERTIES


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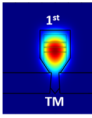


# 1 μM-WAVELENGTH NANO-RIDGE LASER

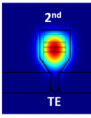


## 1 μM-WAVELENGTH NANO-RIDGE LASER

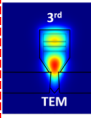
TRANSVERSAL OPTICAL MODES IN NANO-RIDGE WITH 100NM-TRENCH WIDTH



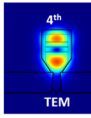
1st  
TM



2nd  
TE



3rd  
TEM

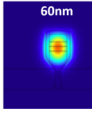


4th  
TEM

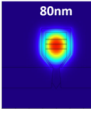
**TE-LIKE GROUND MODE**

- highest confinement in QWs
- lowest leakage loss towards Si substrate is believed to be the dominant waveguide mode. Higher optical gain due to the strain effect

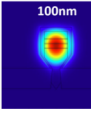
TE-LIKE GROUND MODES IN NANO-RIDGES



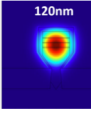
60nm



80nm




100nm



120nm

$n_{eff}$  is increasing for nano-ridges with wider trench

| d (nm) | $n_{eff}$ |
|--------|-----------|
| 60     | 3.022     |
| 80     | 3.122     |
| 100    | 3.167     |
| 120    | 3.210     |

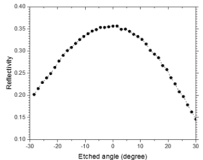


Y. Shi, OPTICA, 4(12), p.1468-1473 (2017) 27

## FABRY PEROT (FP) LASER

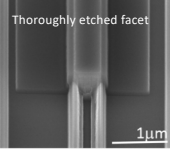
Optical excitation  
5ns pulses at 532nm

**FACET REFLECTIVITY SIMULATION**



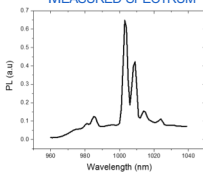
Etched angle (degree)

**CAVITY LENGTH = 180 μm**



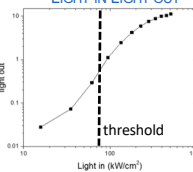
1 μm

**MEASURED SPECTRUM**




Wavelength (nm)

**LIGHT-IN LIGHT-OUT**



Light in (kW/cm<sup>2</sup>)

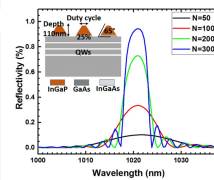
threshold



## λ/4-SHIFT INDEX COUPLED DFB LASER

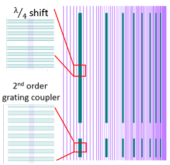
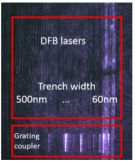
Optical excitation  
5ns pulses at 532nm

**GRATING REFLECTIVITY SIMULATION**

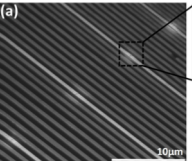


Wavelength (nm)

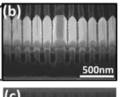
**DFB CAVITY ON DIFFERENT NANO-RIDGE**

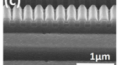
DFB lasers  
Trench width  
500nm ... 60nm  
Grating coupler



(a)




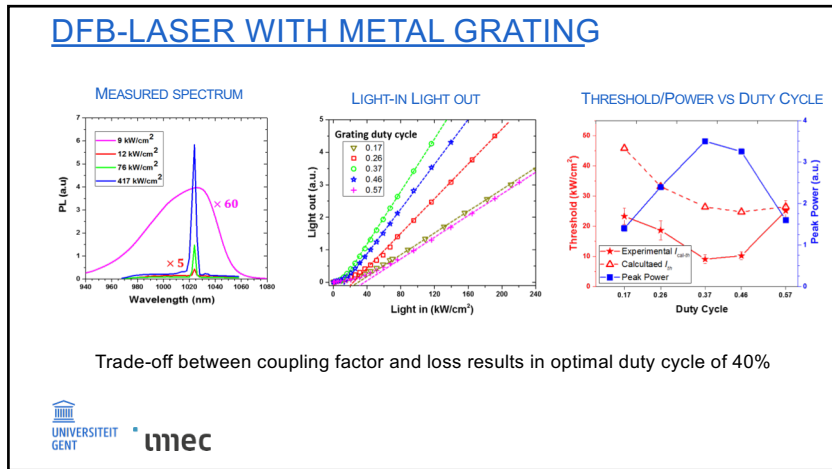
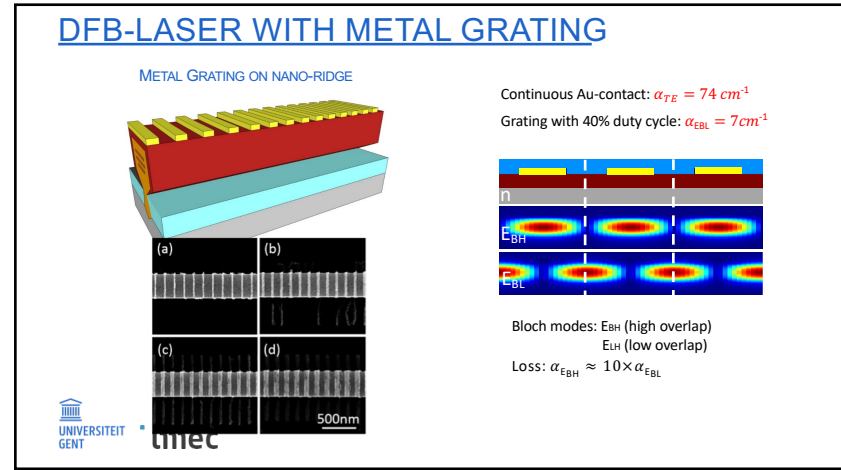
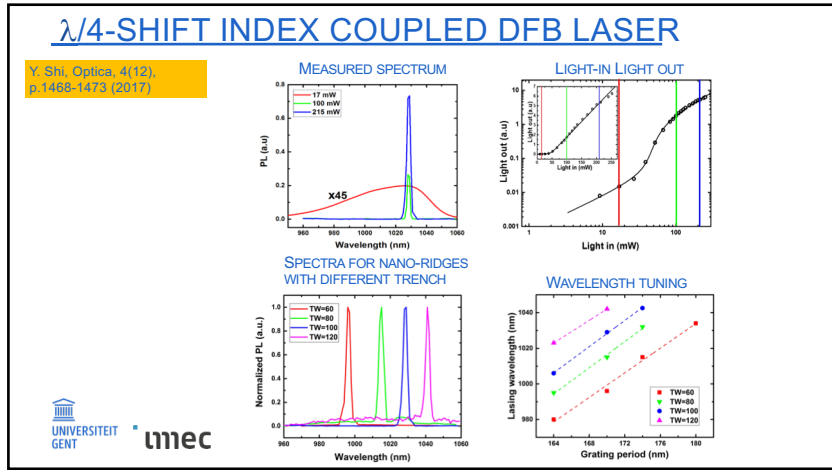
(b)  
500nm



(c)  
1 μm

SAMPLE FABRICATED BY EBEAM-LITHO AND ICP

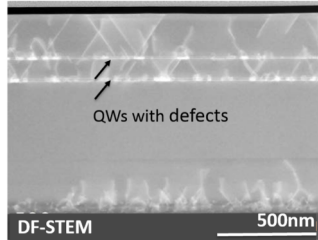






### APPROACH 1

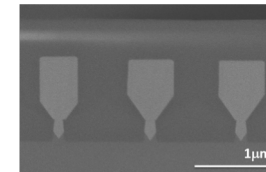
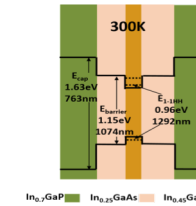
- Increase Indium content in QW's to 45%



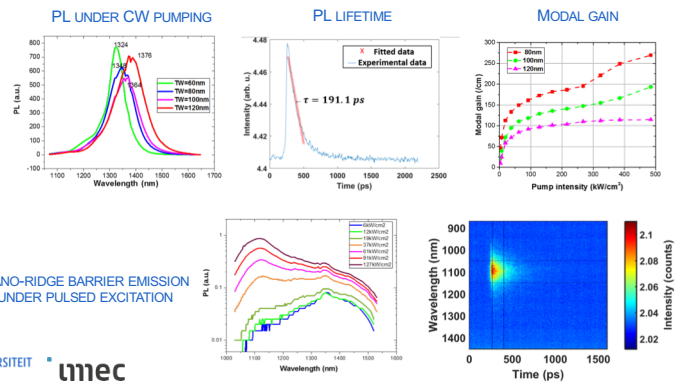
Huge strain leads to new threading dislocations appearing at QW-layers

### APPROACH 2

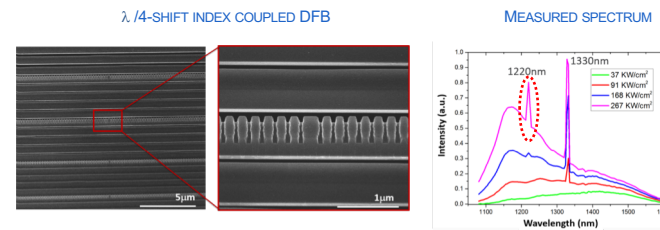
- Replace GaAs buffer by InGaAs Buffer



### CHARACTERIZATION OPTICAL PROPERTIES



### OPTICAL PUMPING λ/4-SHIFTED CAVITY

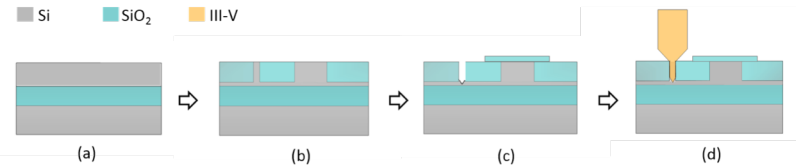


Lasing ?

# FUTURE: COUPLING WITH SILICON WAVEGUIDES?



## PROPOSED INTEGRATION FLOW

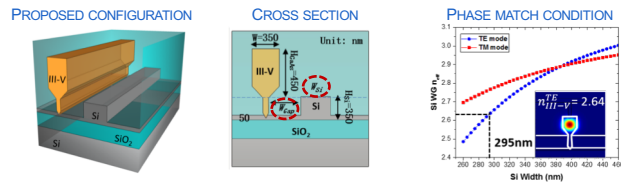


- (a) SOI wafer with 300nm silicon layer
- (b) STI-process defines trench and waveguide
- (c) KOH etch to open trench. Waveguide is protected
- (d) III-V epitaxy

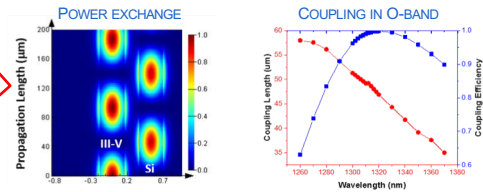


Y. Shi, Optics Express 27(26), p.37781-37794 (2019)

## 1<sup>ST</sup> GENERATION: DIRECTIONAL COUPLER

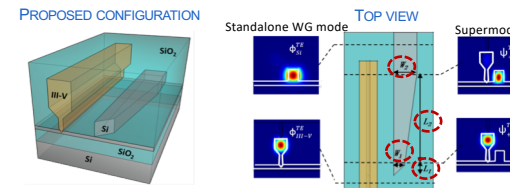


100% coupling  
Sensitive to variation

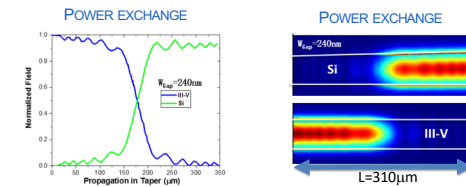


40

## 2<sup>ND</sup> GENERATION: LINEARLY TAPERED COUPLER

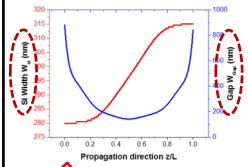


98% coupling  
Long footprint

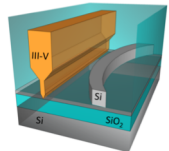


41

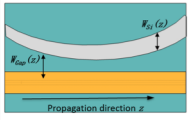
### 3RD GENERATION: OPTIMIZED ADIABATIC COUPLER



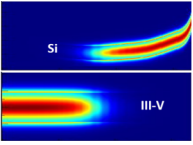
**PROPOSED CONFIGURATION**



**TOP VIEW**



**POWER EXCHANGE**



**BLACKMAN FUNCTION**

$$\kappa(z) = \kappa_{max} \cdot \text{sin}\theta(z)$$

$$\Delta\beta(z) = -\Delta\beta_{max} \cdot \text{cos}\theta(z)$$

$$\theta(z) = \frac{\pi z}{L} - 0.25\text{sin}\frac{2\pi z}{L} - 0.07\text{sin}\frac{4\pi z}{L}$$

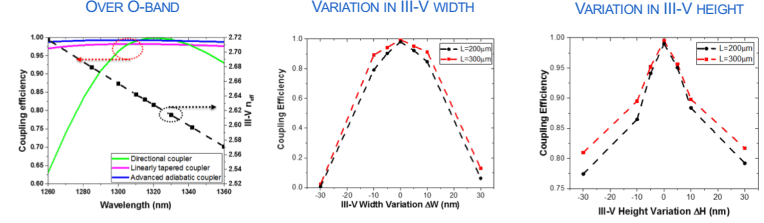
**99% coupling  
Compact size  
High tolerance**

Hsien-kai Hsiao, et al. Midinfrared broadband achromatic astronomical beam combiner for nulling interferometry. *Applied optics*, 49(35):6675–6688, 2010.

Y. Shi, *Optics Express* 27(26), p.37781–37794, (2019)

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### TOLERANCE TO VARIATIONS



**OVER O-BAND**

**VARIATION IN III-V WIDTH**

**VARIATION IN III-V HEIGHT**

**POSSIBLE TO COUPLE WITH SI DEVICES  
GOOD CONTROL ON THE SIZE OF NANO-RIDGE DURING GROWTH**

Y. Shi, *Optics Express* 27(26), p.37781–37794 (2019)


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# CONCLUSION AND PERSPECTIVES

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### CONCLUSION

- Aspect Ratio Trapping combined with nanoridge engineering allows to grow excellent material on 300 mm wafers
- We demonstrated:
  - PL-lifetime > 1 ns
  - Material gain > 4000 cm<sup>-1</sup>
  - Optically pumped lasing with high SMSR at 1.05 µm
    - With both index and loss coupled gratings
  - Emission at 1.3 µm
  - Efficient coupling scheme to silicon waveguides



PhD-thesis Yuting Shi, available from <http://photonics.intec.ugent.be/contact/people.asp?ID=425>

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### 5<sup>th</sup> ePIXfab Silicon Photonic Summer School Ghent University (Belgium)

**DATE** :15 – 19 June 2020

**KEY FEATURES**

- Learn all about silicon photonics: from technology to applications
- Geared towards industrial and academic participants
- A perfect blend of learning and networking

**MORE INFO:**  
 e-mail: [info@epixfab.eu](mailto:info@epixfab.eu)  
 web: <https://epixfab.eu/epixfab-silicon-photonics-summer-school-5th-edition/>













**ePIXfab** Promoting silicon photonics science, technology, and applications  
 The European Silicon Photonics Alliance

### Upcoming Activities

**2020**

#### Training Program

ePIXfab trainings are design to address needs of both industrial and academic workforce with the mindset to enable seamless entry into the field of silicon photonics

-  **China-Europe Silicon Photonics Symposium**  
28-30 May 2020, Chongqing, China
-  **Silicon Photonics School, 5<sup>th</sup> edition**  
15-19 June 2020, Ghent University, Belgium
-  **Design Course, 3<sup>rd</sup> edition**  
8 - 12 June 2020, Ghent University, Belgium
-  **Hands-on design software training**  
22 June 2020, ECIO Conference, Paris, France
-  **European Photonic Integration Forum**  
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## GaAs nano-ridge lasers on silicon (Conference Presentation)

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### Abstract

The silicon photonics platform is still missing a native source. Therefore, using a novel epitaxial process based on aspect ratio trapping and nano-ridge engineering we demonstrated a powerful approach to fabricate GaAs-InGaAs lasers directly on a standard silicon substrate. In depth morphological and optical characterisation confirms the high quality of the material. We demonstrated lasing from DFB-type devices with etched gratings and with metal gratings. In the presentation we will also discuss the possibility for coupling to standard silicon waveguides and for extending the emission to longer wavelengths.

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